

AMENDMENTS TO THE CLAIMS

1. (Original) A method for customization of a FPGA-based SoC, the method comprising:

selecting a system component used for customizing the FPGA-based SoC;

configuring said selected system component with parameters for use with the FPGA-based SoC;

propagating said parameters used to configure said selected system component to peer system components; and

configuring said peer system components using said propagated parameters during customization of the FPGA-based SoC.

2. (Original) The method according to claim 1, further comprising configuring the selected system component with parameters used to configure said peer system component.

3. (Original) The method according to claim 2, further comprising the step of propagating said parameter used to configure said peer system component to subsequently selected system components used to configure the FPGA-based SoC.

4. (Original) The method according to claim 1, wherein said selecting step further comprises the step of providing an option for selecting an implementation selected from the group consisting of a hardware implementation and a software implementation.

5. (Original) The method according to claim 1, wherein said step of selecting said system component further comprises selecting a system component from the group consisting of a hardware core and a software core.

6. (Original) The method according to claim 1, wherein the method further comprises the step of initializing only the selected system components that are utilized for customizing the FPGA-based SoC.

7. (Original) A machine readable storage having stored thereon, a computer program having a plurality of code sections, said code sections executable by a machine for causing the machine to perform the steps of:

selecting a system component used for customizing a FPGA-based SoC;

configuring said selected system component with parameters for use with said FPGA-based SoC;

propagating said parameters used to configure said selected system component to peer system components; and

configuring said peer system components using said propagated parameters during customization of said FPGA-based SoC.

8. (Original) The machine readable storage according to claim 7, further comprising sections of code for causing the machine to configure the selected system component with parameters used to configure said peer system component.

9. (Original) The machine readable storage according to claim 8, further comprising sections of code for causing the machine to propagate said parameter used to configure said peer system component to subsequently selected system components used to configure said FPGA-based SoC.

Claims 10-18 Cancelled